

Amendments to the Specification

Please insert the following Paragraph at page 1, line 5:

--This application is a continuation application of U.S. Patent Application Serial No. 09/282,523 entitled "Successive Approximation Calibration Apparatus, System, and Method for Dynamic Range Extender" having inventor Nadi R. Itani.--

Please replace Paragraph beginning at page 1, line 6 with the amended paragraph as follows:

--This application is related to Patent Application Serial Nos. 09/283,779, 09/283,098, 09/282,524, 09/282,515, and 09/283,112, respectively entitled "Dynamic Range Extender Apparatus, System, and Method for Digital Receiver System" having inventors Sandra Marie Johnson and Nadi Rafik Itani and issued as U.S. Patent No. 6,252,536 on June 26, 2001 (Cirrus Logic Docket No. 0930-CIC); "Phase Locked Loop Circuits, Systems, and Methods" having inventors Douglas R. Holberg and Sandra Marie Johnson and issued as U.S. Patent No. 6,617,934 on September 9, 2003 (Cirrus Logic Docket No. 0893-CIC); "Preview Mode Low Resolution Output System and Method" having inventors Douglas R. Holberg, Sandra Marie Johnson, and Nadi Rafik Itani and issued as U.S. Patent No. 6,686,957 on February 3, 2004 (Cirrus Logic Docket No. 0944-CIC); "Amplifier System with Reducable Power" having as inventor, Nadi Rafik Itani (Cirrus Logic Docket No. 0965-CA) and is abandoned; "CCD Imager Analog Processor Systems and Methods" having inventors Douglas R. Holberg, Sandra Marie Johnson, Nadi Rafik Itani, and Argos R. Cue (Cirrus Logic Docket No. 0929-CIC). Each of the above applications is filed on even date herewith. Additionally, this application is related to Patent Application Serial No. 08/856,468, entitled "System and Method for Enhancing Dynamic Range in Images" invented by S. Khalid Azim and issued as U.S. Patent No. 6,137,533 on October 24, 2000 (Cirrus Logic No. 0720-CIC). Each of these applications is incorporated herein by reference in its entirety.--.

Please replace Paragraph beginning at page 5, line 31 with the amended paragraph as follows:

--Figure 8A is a block diagram of a circuit system in a controllable dynamic range extension signal processing (DRX) circuit including a correlated double sampling (CDS) circuit for receiving the video signal from the CCD imaging device, including a calibration register, in accordance with an embodiment of the present invention.--